


12/27/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>  </p> <p> O.I.P.E. </p> <p> SCANNED <u>UR</u> (3) Q.A. <u>AM</u> </p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/748256	CONT/PRIOR D	CLASS 438 257	SUBCLASS 301	ART UNIT 28.12	EXAMINER R. L. Nadeau
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APPLICANTS

Howard Chen
Louis Hsu
Li-Kong Wang

TITLE

Method for fabricating complementary metal oxide semiconductor (CMOS) devices on a mixed bulk and silicon-on-insulator (SOI) substrate

PTO-2040
12/99

CPA

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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